

1/10

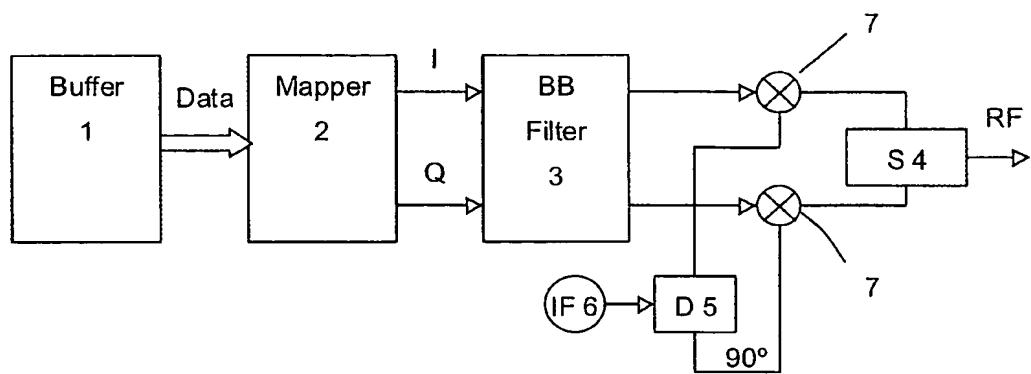


Fig. 1

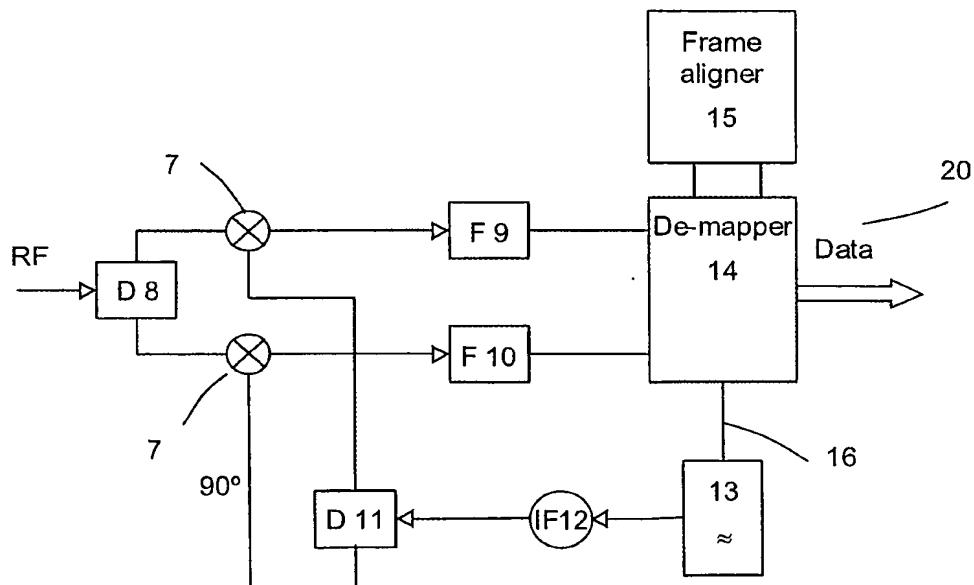


Fig. 2

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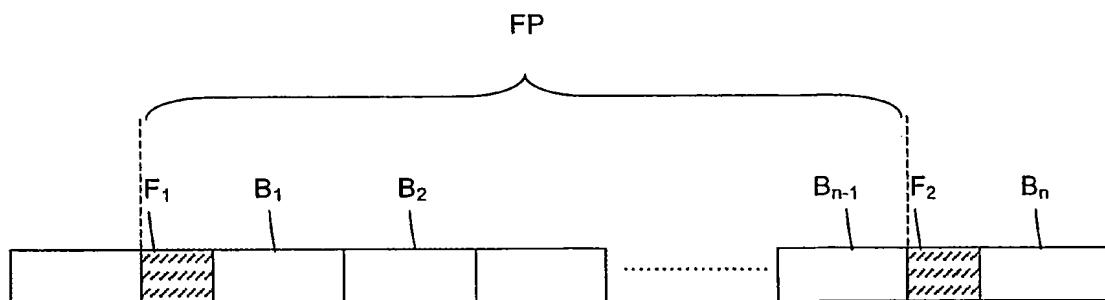


Fig. 3

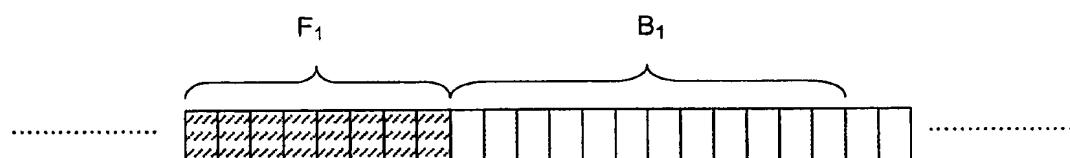


Fig. 4

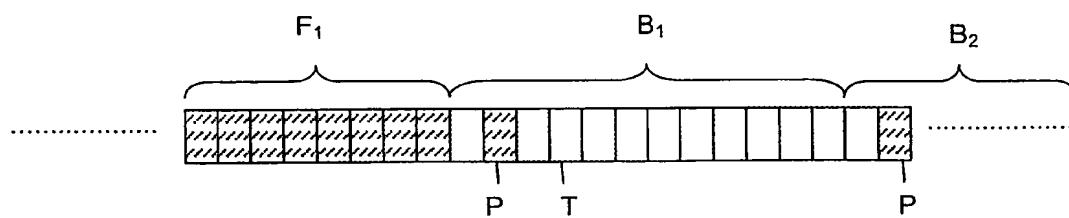


Fig. 4'

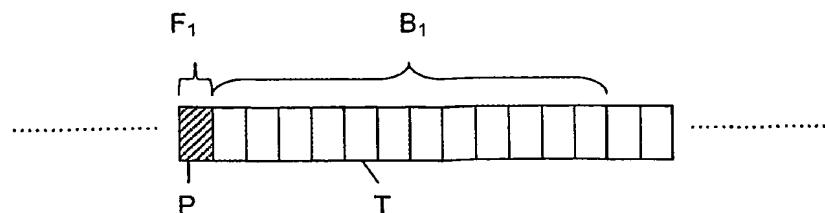
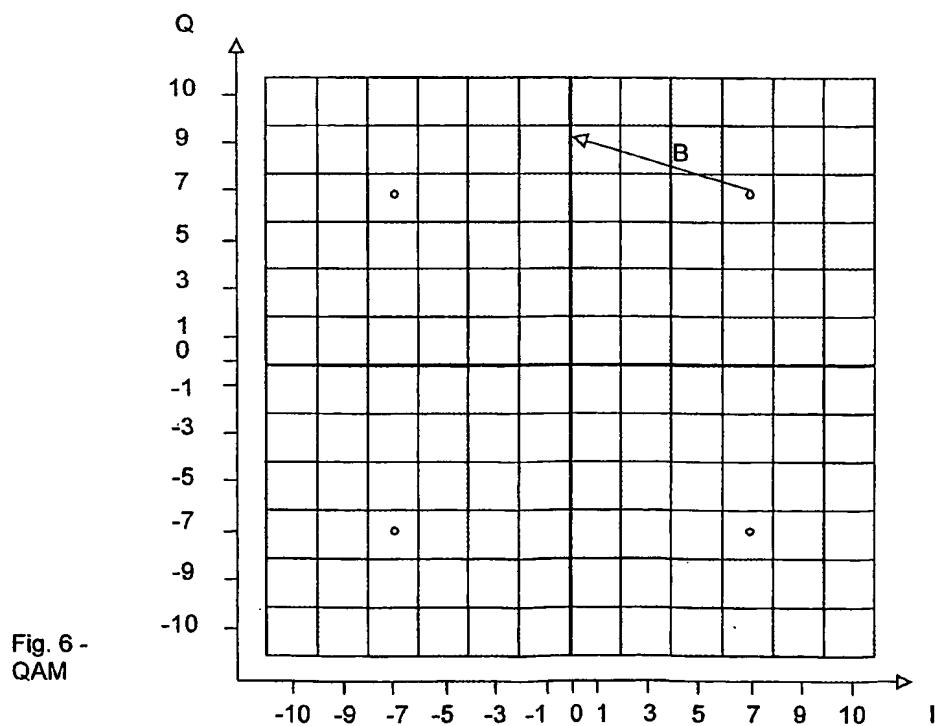
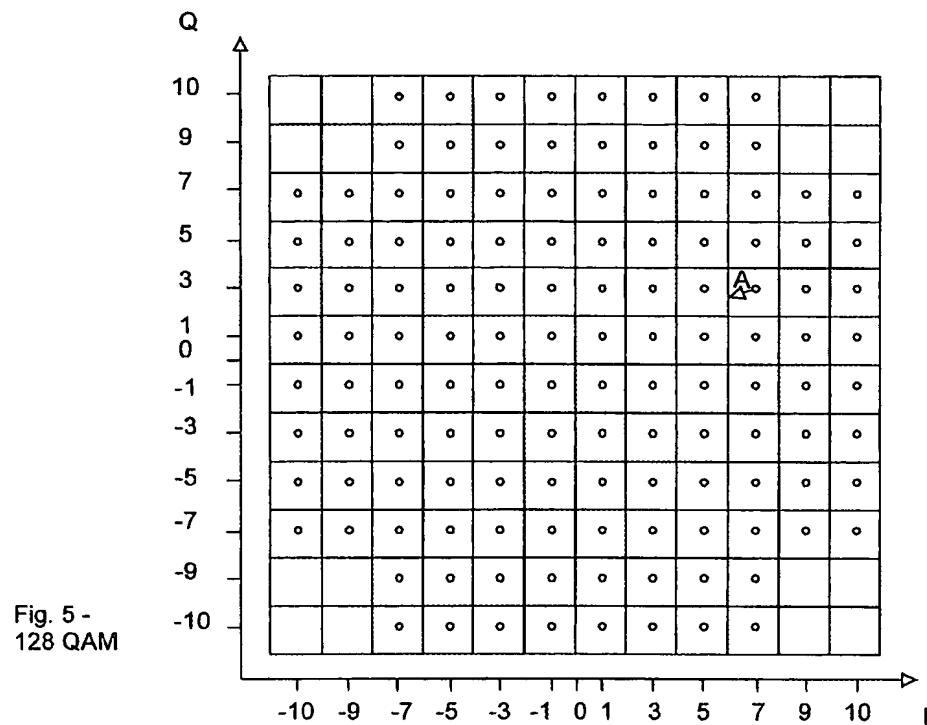
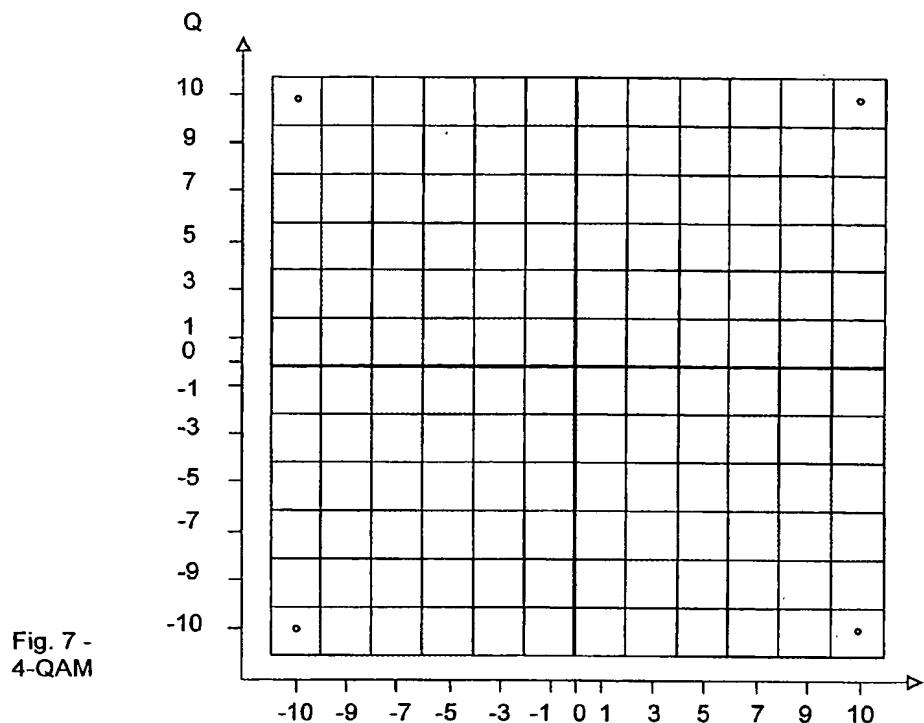


Fig. 4''

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sequence order	n-1	n	n+1	n+...	n+t	n+t+1
modu- lation	4	128	128	...	4	128
data bit	2	7	7	....	2	7

Fig. 8

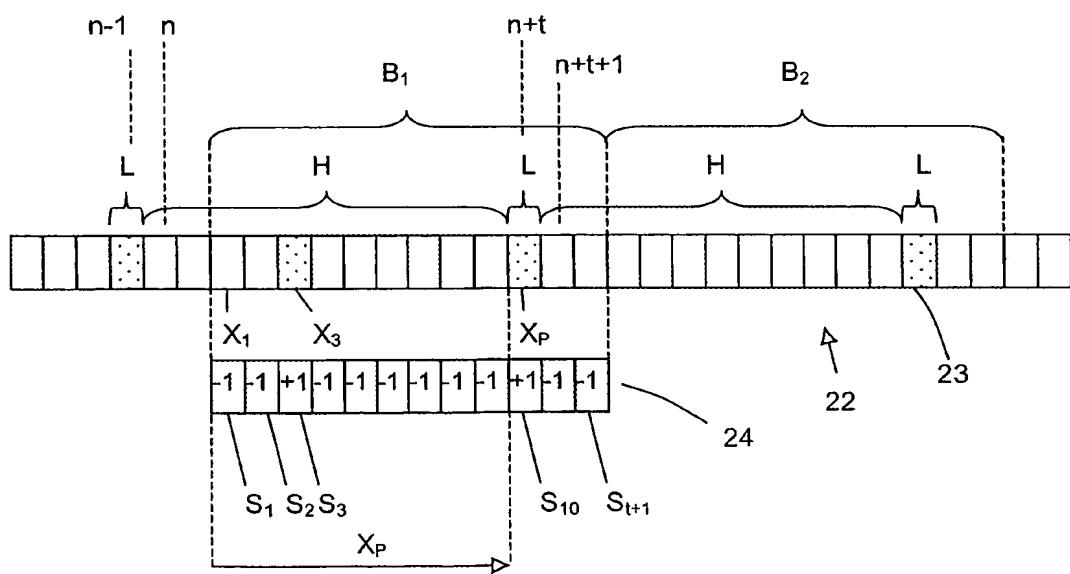


Fig. 9

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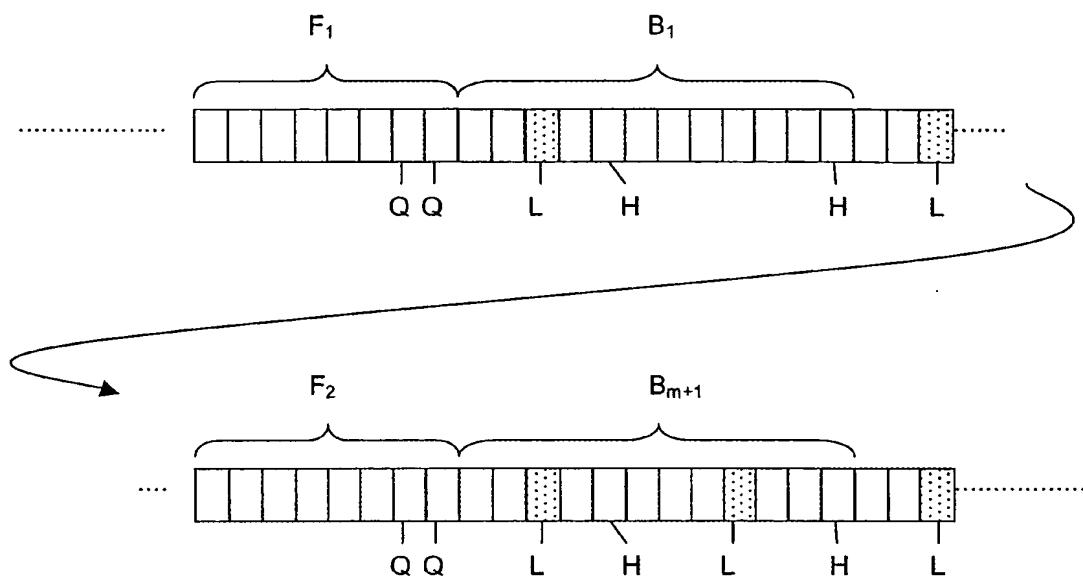


Fig. 10

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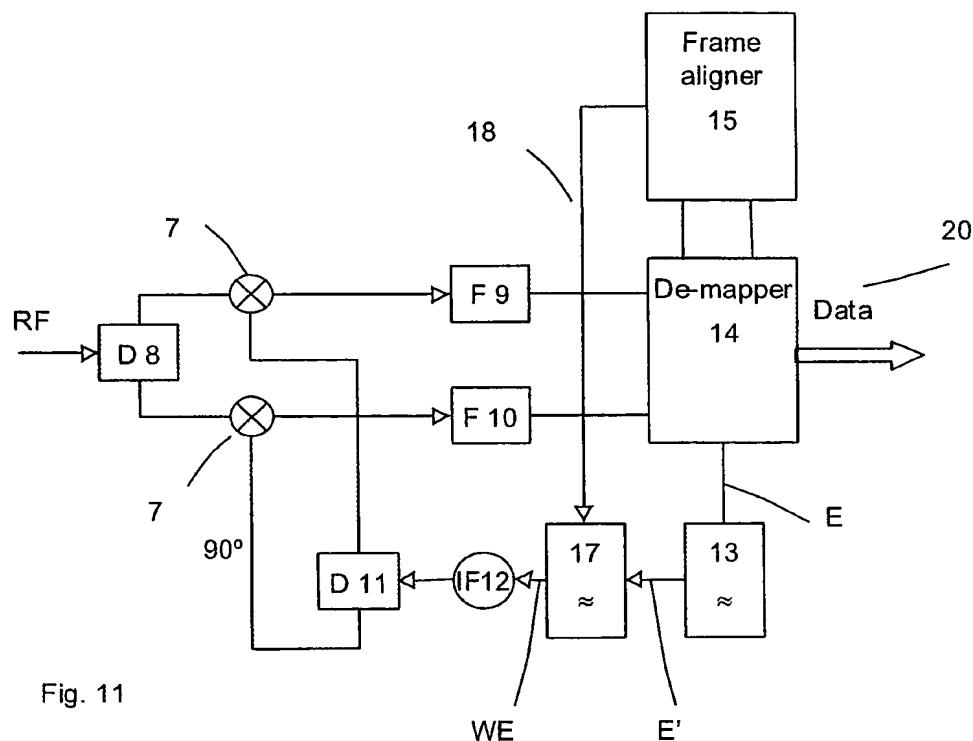


Fig. 11

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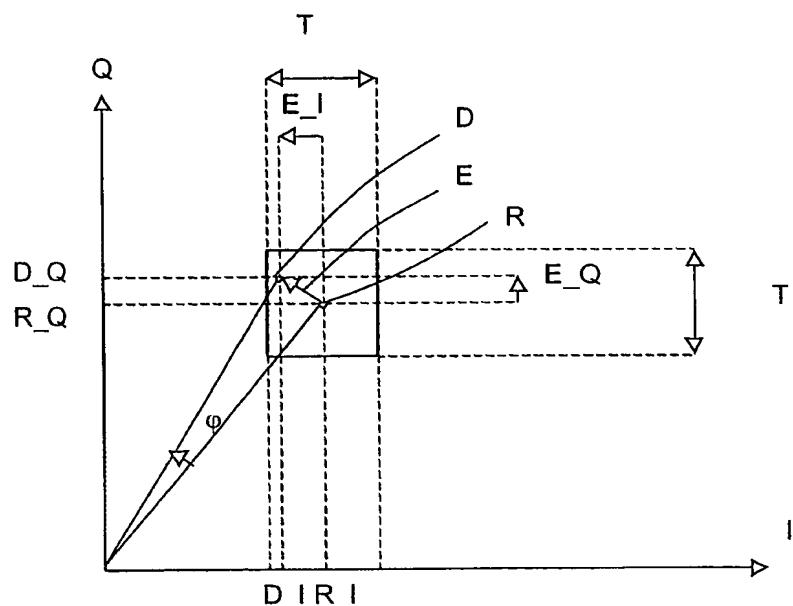


Fig. 12

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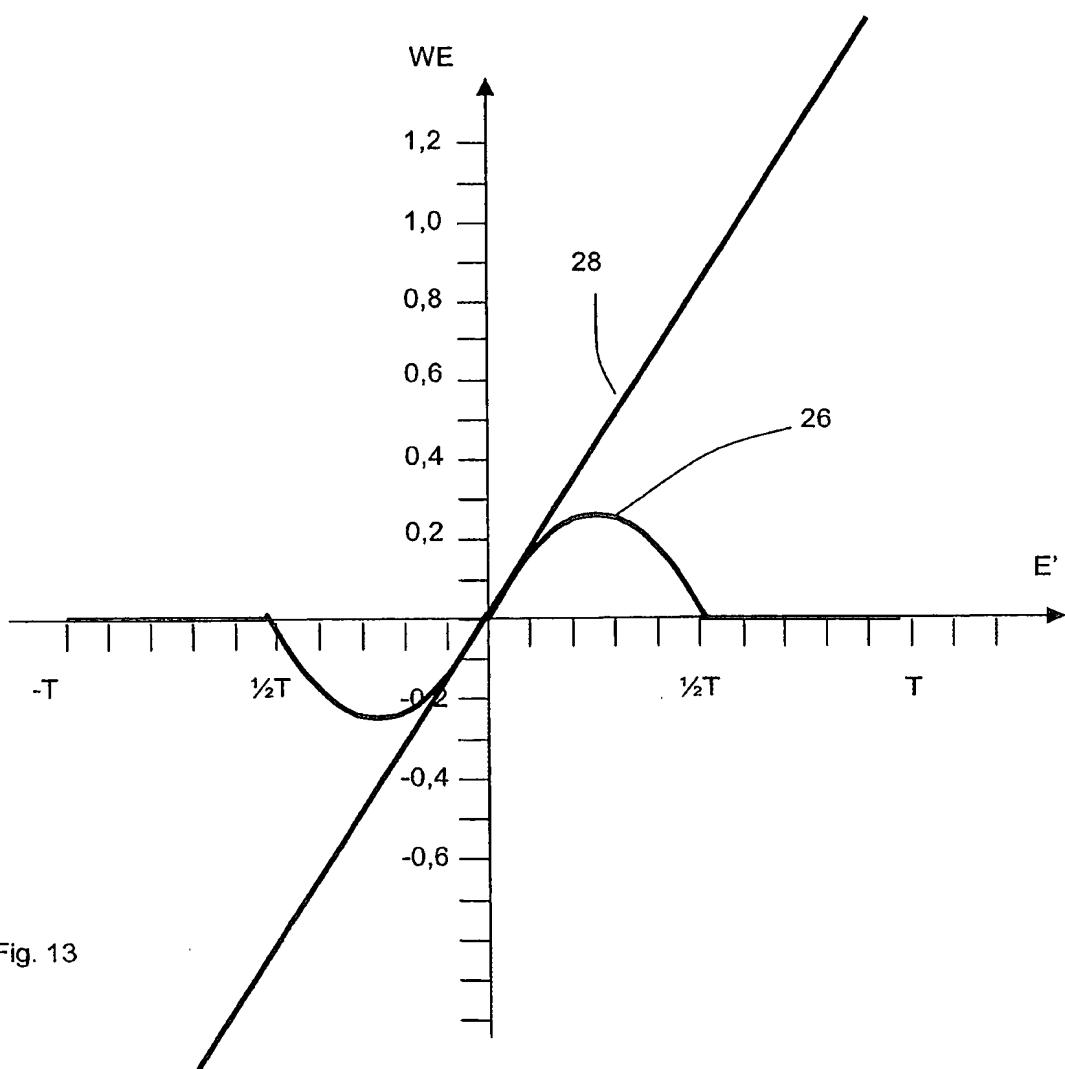


Fig. 13

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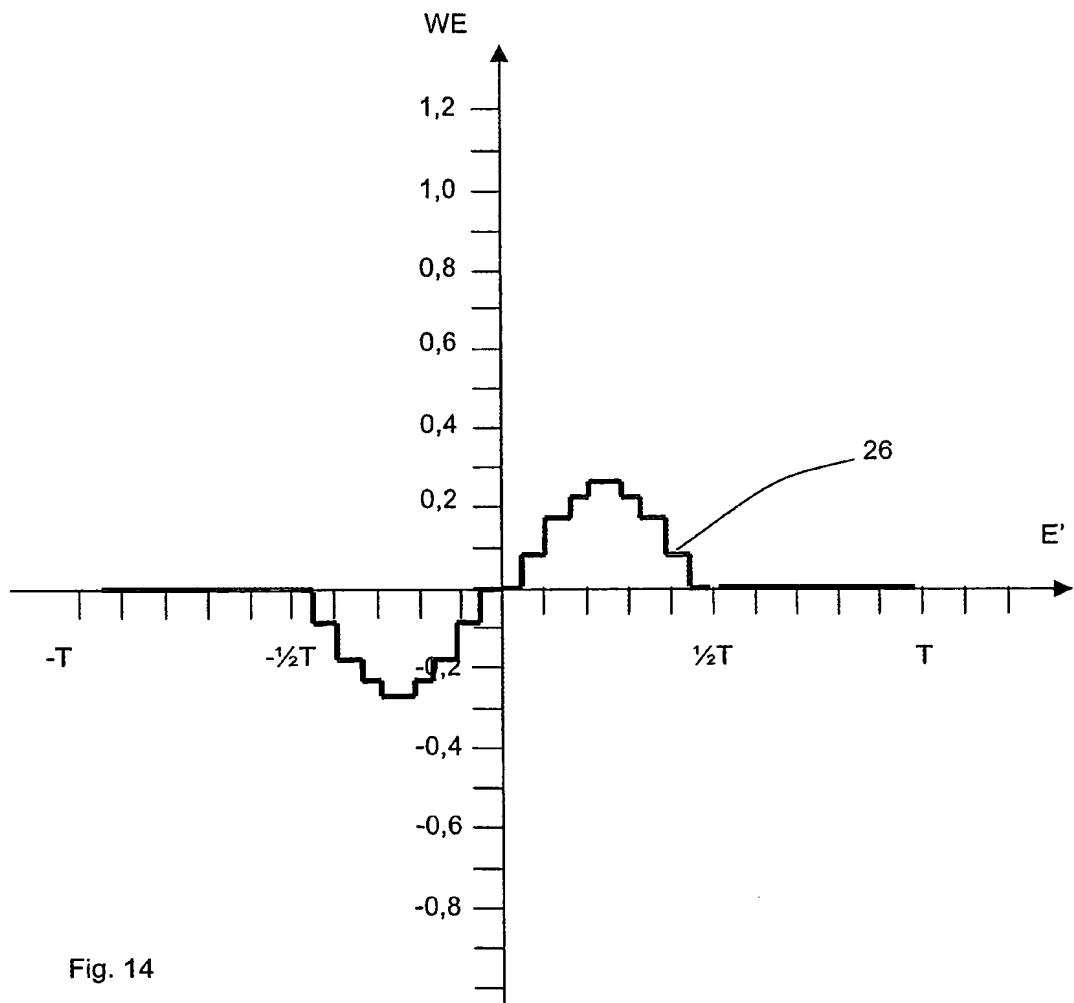


Fig. 14